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Freescale Semiconductor, Inc.
Law Department
7700 West Parmer Lane
MD:TX32/PL02
Austin, Texas 78729

Telephone: (512) 996.6839
Facsimile: (512) 996.6854
Email:

10 Number of Pages (including this page)

Date: August 3, 2005

To: Examiner Naum B. Levin - Group 2825

Location: United States Patent and Trademark Office

Fax No.: (703) 872-9306 (512) 273-8300

From: Ranjeev Singh (Registration No. 47,093)

Subject: Serial No. 10/657,304 - Kiran K. Gullapalli et al.

MESSAGE: Enclosed herewith, please find a formal amendment for filing in the below-identified application. If Applicant has overlooked any additional fees, or if any overpayment has been made, the Commissioner is hereby authorized to credit or debit Deposit 503079, Freescale Semiconductor, Inc.

THE FOLLOWING MARKED WITH AN "X" ARE INCLUDED IN THE FAX

1.	x	1 page Fax cover sheet
2.	X	9 page Amendment

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PLEASE GIVE THESE PAPERS TO:

EXAMINER: Naum B. Levin
GROUP ART UNIT 2825
SERIAL NO.: 10/657,304
FILED: September 8, 2003
INVENTOR: Kiran K. Gullapalli et al.

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UNITED STATES PATENT AND TRADEMARK OFFICE

APPLICANT(S) Kiran K. Gullapalli et al. GROUP ART 2825
UNIT:
APPLN. NO.: 10/657,304 EXAMINER: Naum B. Levin
FILED: September 8, 2003 DOCKET NO: SC12003TS
TITLE: METHOD AND APPARATUS FOR DISTORTION ANALYSIS IN
NONLINEAR CIRCUITS

Certificate of Transmission under 37 CFR 1.8

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on August 3, 2005

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AMENDMENT

Box Non-Fee Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

This paper is responsive to the Office Action dated May 23, 2005. Further examination and reconsideration of this application are respectfully requested in view of the amendments and remarks below.

Please amend the subject application as follows: